

The documentation and process conversion measures necessary to comply with this document shall be completed by 28 December 2019.

INCH-POUND

MIL-PRF-19500/260N
 28 August 2019
 SUPERSEDING
 MIL-PRF-19500/260M
 30 October 2014

PERFORMANCE SPECIFICATION SHEET

SEMICONDUCTOR DEVICE, DIODE, SILICON, POWER RECTIFIER, ENCAPSULATED (CASE MOUNT STUD) AND UNENCAPSULATED, TYPES 1N1202A, 1N1204A, 1N1206A, 1N3671A, 1N3673A, AND AR VERSIONS, QUALITY LEVELS JAN, JANTX, JANTXV, JANS, JANHC, AND JANKC, ENCAPSULATED (CASE MOUNT STUD), 1N1124A, 1N1126A, 1N1128A, 1N3649, 1N3650, AND R VERSIONS, QUALITY LEVEL JAN ONLY

Types 1N1124A, 1N1126A, 1N1128A, 1N3649, 1N3650 are inactive for new design after 7 June 1999.

This specification is approved for use by all Departments and Agencies of the Department of Defense.

The requirements for acquiring the product described herein shall consist of this specification sheet and [MIL-PRF-19500](#).

1. SCOPE

1.1 Scope. This specification covers the performance requirements for silicon semiconductor power rectifier diodes. Four levels of product assurance (JAN, JANTX, JANTXV, and JANS) are provided for encapsulated device types 1N1202A, 1N1204A, 1N1206A, 1N3671A, 1N3673A and two levels of product assurance (JANHC, AND JANKC) for each unencapsulated device type as specified in [MIL-PRF-19500](#). One level of product assurance (JAN) is provided for device types 1N1124A, 1N1126A, 1N1128A, 1N3649, and 1N3650 as specified in [MIL-PRF-19500](#).

1.2 Physical dimensions. See [figure 1](#) (DO-203AA, formerly DO-4) and [figure 2](#) (JANHC and JANKC).

1.3 Maximum ratings. Unless otherwise specified, T_C = +25°C.

Type (1)	V _{RWM}	I _o T _C = 150°C	I _{f(surge)} T _C = 150°C T = 1/120 s	Barometric pressure (reduced)	T _J and T _{STG}	R _{θJC}
	<u>V dc</u>	<u>A dc</u>	<u>A pk</u>	<u>mmHg</u>	<u>°C</u>	<u>°C/W max</u>
1N1202A (2)	200	12	240	N/A	-65 to +200	2.0
1N1204A (2)	400	12	240	8	-65 to +200	2.0
1N1206A (2)	600	12	240	16	-65 to +200	2.0
1N3671A (2)	800	12	240	30	-65 to +200	2.0
1N3673A (2)	1000	12	240	54	-65 to +200	2.0
1N1124A (3)	200	3.3	25	8	-65 to +175	2.0
1N1126A (3)	400	3.3	25	8	-65 to +175	2.0
1N1128A (3)	600	3.3	25	16	-65 to +175	2.0
1N3649 (3)	800	3.3	25	30	-65 to +175	2.0
1N3650 (3)	1,000	3.3	25	54	-65 to +175	2.0

See notes on next page.

Comments, suggestions, or questions on this document should be addressed to DLA Land and Maritime, ATTN: VAC, P.O. Box 3990, Columbus, OH 43218-3990, or emailed to Semiconductor@dla.mil. Since contact information can change, you may want to verify the currency of this address information using the ASSIST Online database at <https://assist.dla.mil>.

AMSC N/A

