



IPC-7525C

2021 - November

Stencil Design Guidelines

Supersedes IPC-7525B
October 2011

A standard developed by IPC



BUILD ELECTRONICS BETTER

The Principles of Standardization

In May 1995 the IPC's Technical Activities Executive Committee (TAEC) adopted Principles of Standardization as a guiding principle of IPC's standardization efforts.

Standards Should:

- Show relationship to Design for Manufacturability (DFM) and Design for the Environment (DFE)
- Minimize time to market
- Contain simple (simplified) language
- Just include spec information
- Focus on end product performance
- Include a feedback system on use and problems for future improvement

Standards Should Not:

- Inhibit innovation
- Increase time-to-market
- Keep people out
- Increase cycle time
- Tell you how to make something
- Contain anything that cannot be defended with data

Notice

IPC Standards and Publications are designed to serve the public interest through eliminating misunderstandings between manufacturers and purchasers, facilitating interchangeability and improvement of products, and assisting the purchaser in selecting and obtaining with minimum delay the proper product for his particular need. Existence of such Standards and Publications shall not in any respect preclude any member or nonmember of IPC from manufacturing or selling products not conforming to such Standards and Publication, nor shall the existence of such Standards and Publications preclude their voluntary use by those other than IPC members, whether the standard is to be used either domestically or internationally.

Recommended Standards and Publications are adopted by IPC without regard to whether their adoption may involve patents on articles, materials, or processes. By such action, IPC does not assume any liability to any patent owner, nor do they assume any obligation whatever to parties adopting the Recommended Standard or Publication. Users are also wholly responsible for protecting themselves against all claims of liabilities for patent infringement.

IPC Position Statement on Specification Revision Change

It is the position of IPC's Technical Activities Executive Committee that the use and implementation of IPC publications is voluntary and is part of a relationship entered into by customer and supplier. When an IPC publication is updated and a new revision is published, it is the opinion of the TAEC that the use of the new revision as part of an existing relationship is not automatic unless required by the contract. The TAEC recommends the use of the latest revision. Adopted October 6, 1998

Why is there a charge for this document?

Your purchase of this document contributes to the ongoing development of new and updated industry standards and publications. Standards allow manufacturers, customers, and suppliers to understand one another better. Standards allow manufacturers greater efficiencies when they can set up their processes to meet industry standards, allowing them to offer their customers lower costs.

IPC spends hundreds of thousands of dollars annually to support IPC's volunteers in the standards and publications development process. There are many rounds of drafts sent out for review and the committees spend hundreds of hours in review and development. IPC's staff attends and participates in committee activities, typesets and circulates document drafts, and follows all necessary procedures to qualify for ANSI approval.

IPC's membership dues have been kept low to allow as many companies as possible to participate. Therefore, the standards and publications revenue is necessary to complement dues revenue. The price schedule offers a 50% discount to IPC members. If your company buys IPC standards and publications, why not take advantage of this and the many other benefits of IPC membership as well? For more information on membership in IPC, please visit www.ipc.org or call 847/597-2872.

Thank you for your continued support.



IPC-7525C

Stencil Design Guidelines

Developed by the members of the Solder Stencil Task Group (5-21e) of the Assembly & Joining Committee (5-20) of IPC.

Supersedes:
IPC-7525B - October 2011

Users of this publication are encouraged to participate in the development of future revisions.

Contact:

IPC
3000 Lakeside Drive, Suite 105N
Bannockburn, Illinois
60015-1249
Tel 847 615.7100
Fax 847 615.7105

This Page Intentionally Left Blank

Acknowledgment

Any document involving a complex technology draws material from a vast number of sources. While the principal members of the Stencil Design Task Group (5-21e) of the Assembly and Joining Processes Committee (5-20) are shown below, it is not possible to include all of those who assisted in the evolution of this standard. To each of them, the members of the IPC extend their gratitude.

Assembly and Joining Processes Committee

Chair:

Daniel Foster
Missile Defense Agency (MDA)

Solder Stencil Task Group (5-21e)

Chair:

Jeff Schake
ASM Assembly Systems

Vice Chair:

Greg Smith
BlueRing Stencils

Technical Liaisons of the IPC Board of Directors

Bob Neves

Microtek (Changzhou) Laboratories

Component Mounting Subcommittee

Daniel Fillipe Barros Alves
Bosch Car Multimedia Portugal, S.A

Dudi Amir
Intel Corporation

Gerald Leslie Bogert
Bechtel Plant Machinery, Inc.

Lance Brack
Raytheon Missile Systems

Robin Bridge
Curtiss-Wright Defense Solutions

Edgar Butron
Lockheed Martin

Zhiman Chen
Zhuzhou CRRC Times
Electric Co., Ltd.

Robert E. Cochran
Mind Chasers Inc.

Danielle L. Daniel
Naval Surface Warfare Center

Francesco Di Maio
GESTLABS S.r.l.

Michael Durkan
Mentor Graphics Corporation

Brian Flemming
National Instruments

Joseph E. Kane
BAE Systems

Anthony W. Lentz
FCT Assembly, Inc.

Kyle Loomis
Kester

Timothy O'Neill
AIM Solder

Frank Paradis
Calian Advanced Technologies

Kenneth Rahn
FCI USA, Inc.

Robert Rowland
Axiom Electronics, LLC

Jeff Schake
ASM Assembly Systems

Jeff Shubrooks
Raytheon Company

Greg Smith
BlueRing Stencils

Raymond Whittier
BAE Systems

Michael W. Yuen
Foxconn EMS, Inc.

Jack Zhu
Veoneer China Co., Ltd.

This Page Intentionally Left Blank

Table of Contents

1.0 SCOPE	1	1.8.25 Transfer Efficiency	3
1.1 Purpose	1	1.8.26 Ultra-Fine Pitch Technology	3
1.2 Classification	1	2.0 APPLICABLE DOCUMENTS	3
1.3 Measurement Units	1	2.1 IPC	3
1.4 Definition of Requirements	1	3.0 STENCIL DESIGN	3
1.5 Order of Precedenc	1	3.1 Stencil Data	3
1.5.1 Conflict	1	3.1.1 Data Format	3
1.5.2 Clause References	1	3.1.2 Gerber® Format	4
1.5.3 Appendices	1	3.1.3 Aperture List	4
1.6 Use of “Lead”	2	3.1.4 Solder Paste Layer	4
1.7 Abbreviations and Acronyms	2	3.1.5 Data Transfer	4
1.7.1 PCB Printed Circuit Board	2	3.1.6 Panelized Stencils	4
1.7.2 BGA Ball Grid Array	2	3.1.7 Step-and-Repeat	4
1.7.3 FPT Fine-Pitch Technology	2	3.1.8 Image Orientation/Rotation	4
1.7.4 SMT Surface Mount Technology	2	3.1.9 Image Location	4
1.7.5 THT Through-Hole Technology	2	3.1.9.1 Multiple Assembly Images	5
1.8 Terms and Definitions	2	3.1.10 Identification	5
1.8.1 *Aperture	2	3.2 Aperture Design	5
1.8.2 *Area Ratio	2	3.2.1 Aperture Size	5
1.8.3 *Aspect Ratio	2	3.2.1.2 Area Ratio/Aspect Ratio	5
1.8.4 Border	2	3.2.2 Aperture Size versus Board Land Size f or Tin Lead Solder Paste	12
1.8.5 Enclosed Print Head	2	3.2.2.1 Terminal Leaded SMDs	12
1.8.6 Etch Factor	2	3.2.2.2 Plastic BGAs	12
1.8.7 Relief Etch	2	3.2.2.3 Ceramic Grid Arrays	12
1.8.8 Fiducials	2	3.2.2.4 Fine and Ultra-Fine Pitch BGA and CSP	12
1.8.9 Fine-Pitch Ball Grid Array (BGA)	2	3.2.2.5 Discrete Components – Resistors and Capacitors	12
1.8.10 Fine-Pitch Technology (FPT)	2	3.2.2.6 Cylindrical, Mini-MELF and Discrete Components	12
1.8.11 Foil	2	3.2.2.7 LCC/BTC Devices	12
1.8.12 Frame	2	3.2.3 Aperture Size versus Board Land Size for Lead Free Solder Paste	13
1.8.13 Intrusive Soldering	2	3.2.3.1 Terminal Leaded SMDs	13
1.8.14 *Land	2	3.2.3.2 Plastic BGAs	13
1.8.15 Modification	2	3.2.3.3 Ceramic Grid Arrays	13
1.8.16 *Overprinting	2	3.2.3.4 Fine and Ultra-Fine Pitch BGA and CSP	13
1.8.17 *Pad See “Land”	2	3.2.3.5 Discrete Components - Resistors and Capacitors	14
1.8.18 Squeegee	3	3.2.3.6 MELF, Mini-MELF Components	14
1.8.19 Squeegee Direction	3	3.2.3.7 BTC/LCC Devices	14
1.8.20 Standard BGA	3	3.2.4 Glue Aperture Discrete Component	14
1.8.21 *Stencil	3		
1.8.22 Step Stencil	3		
1.8.23 *Surface-Mount Technology (SMT)	3		
1.8.24 Through-Hole Technology (THT)	3		

3.2.5	Glue Apertures for Combination of Discrete Components and Terminal Leaded Devices	14
3.2.6	Relief Etch with Glue Stencils	14
3.3	Mixed Technology Surface-Mount/Through-Hole (Intrusive Soldering)	15
3.3.1	Solder Paste Volume	15
3.4	Mixed Technology Surface-Mount/Flip Chip	16
3.4.1	Two-Print Stencil for Surface-Mount/Flip Chip	16
3.5	Step Stencil Design	17
3.5.1	Step-Down Stencil	17
3.5.2	Step-Up Stencil	17
3.5.3	Step Stencil for Enclosed Print Heads	18
3.5.4	Relief-Etch Stencil	18
3.6	Fiducials	18
3.6.1	Global Fiducials	18
3.6.2	Local Fiducials	18
3.7	Rework and Repair Stencils	19
3.7.1	Mini Stencils	19
3.7.2	Repair Tool for Printing Paste Directly on the Component	19
4.0	STENCIL FABRICATION	19
4.1	Foils	19
4.2	Frames	19
4.3	Stencil Border	19
4.4	Stencil Fabrication Technologies	19
4.4.1	Chemical Etch	19
4.4.1.1	High-Precision Etch	19
4.4.2	Laser-Cut Stencils	19
4.4.3	Electroform	19
4.4.4	Hybrid	20
4.4.5	Trapezoidal Apertures	20
4.4.6	Additional Options	20
5.0	STENCIL MOUNTING	20
5.1	Location of Image on Metal	20
5.2	Centering	20
5.3	Additional Design Guidelines	20
6.0	STENCIL ORDERING	20
7.0	STENCIL USER'S INSPECTION/VERIFICATION	21
8.0	STENCIL CLEANING	21
9.0	END OF LIFE	21

Tables

Table 3-1	Stencil Use Clauses	5
Table 3-2	General Aperture Design Guideline Examples for Selective Surface-Mount Devices	6
Table 3-3	Process Window for Intrusive Soldering - Maximum Limits Desirable	15

Figures

Figure 3-1a	3mil Thick Stencil Aperture Size Guidelines	7
Figure 3-1b	0.080 mm Thick Stencil Aperture Size Guideline	7
Figure 3-2b	0.100 mm Thick Stencil Aperture Size Guideline	8
Figure 3-3b	0.125 mm Thick Stencil Aperture Size Guideline	9
Figure 3-4b	0.150 mm Thick Stencil Aperture Size Guideline	10
Figure 3-5b	0.200 mm Thick Stencil Aperture Size Guideline	11
Figure 3-7	Home Plate Aperture Design	12
Figure 3-6	Cross-Sectional View of a Stencil	12
Figure 3-8	Bow Tie Aperture Design	13
Figure 3-10	Aperture Design for Cylindrical Components and Chip Components (All Corners Rounded)	13
Figure 3-9	Oblong Aperture Design	13
Figure 3-12	Glue Stencil Aperture Design	14
Figure 3-14	Print Only Mode 15 mil Thick Stencil	14
Figure 3-13	Chip Component and SOIC Present on Board	14
Figure 3-15	Glue Stencil with Glue Reservoir	14
Figure 3-16	Through-Hole Solder Paste Volume	15
Figure 3-17	Overprint Without Step	16
Figure 3-18	Overprint With Step (Squeegee Side)	16
Figure 3-19	Overprint With Step (Contact/Board Side)	16
Figure 3-22	Print With Step	17
Figure 3-20	Two-Print Through-Hole Stencil	17
Figure 3-21	Two-Print Through-Hole Stencil	17
Figure 3-23	Step Down	18
Figure 3-24	Step Up	18
Figure 4-1	Trapezoidal Apertures	19

Stencil Design Guidelines

1.0 SCOPE

1.1 Purpose This document provides guidance for the design and fabrication of stencils for solder paste and surface-mount adhesive. It is intended as a guideline only. Much of the content is based on the experience of stencil designers, fabricators, and users. Printing performance depends on many different variables and therefore no single set of design rules can be established. Although this Handbook uses mandatory terminology, e.g., **shall**, **must**, etc., nothing within this Handbook is considered mandatory unless this document is specified as a mandatory requirement in the contract documentation.

1.2 Classification There are three general Performance Classes

CLASS 1 General Electronic Products

- Includes products suitable for applications where the major requirement is function of the completed assembly.

CLASS 2 Dedicated Service Electronic Products

- Includes products where continued performance and extended life is required, and for which uninterrupted service is desired but not critical. Typically, the end-use environment would not cause failures.

CLASS 3 High Performance/Harsh Environment Electronic Products

- Includes products where continued high performance or performance-on-demand is critical, equipment downtime cannot be tolerated, end-use environment may be uncommonly harsh, and the equipment must function when required, such as life support or other critical systems.

1.3 Measurement Units All dimensions and tolerances in this specification are expressed in precise SI (metric) units and bracketed soft imperial [inch] units. Users of this specification are expected to use metric dimensions. All dimensions ≥ 1 mm [0.0394 in] will be expressed in millimeters and inches. All dimensions < 1 mm [0.0394 in] will be expressed in micrometers and microinches.

1.4 Definition of Requirements The words **shall** or **shall not** are used in the text of this document wherever there is a requirement for materials, preparation, process control or acceptance.

The word “should” reflects recommendations and is used to reflect general industry practices and procedures for guidance only.

Line drawings and illustrations are depicted herein to assist in the interpretation of the written requirements of this Standard. The text takes precedence over the figures.

1.5 Order of Precedence The contract **shall** take precedence over this Standard, referenced standards and drawings.

In the event of conflict, the following order of precedence applies:

- 1) Procurement as agreed and documented between customer and supplier.
- 2) Master drawing reflecting the customer’s detailed requirements.
- 3) When invoked by the customer or per contractual agreement, this standard.

When documents other than this standard are cited, the order of precedence **shall** be defined in the procurement documents.

The User has the opportunity to specify alternate acceptance criteria.

1.5.1 Conflict In the event of conflict between the requirements of this standard and the applicable drawing(s) and documentation, the applicable user-approved drawing(s) and documentation govern.

Some examples of documentation include the contract, purchase order, technical data package, engineering specification or performance specification. In the event of a conflict between the text of this standard and the applicable documents cited herein, the text of this standard takes precedence. In the event of conflict between the requirements of this standard and drawing(s) and documentation that has not been user approved, this standard governs.

1.5.2 Clause References When a clause in this document is referenced its subordinate clauses apply, unless the requirement references specific subordinate clauses.

1.5.3 Appendices Appendices to this standard are not binding requirements unless separately and specifically required by this standard, the applicable contracts, assembly drawing(s), documentation or purchase orders.